

10016448 121004

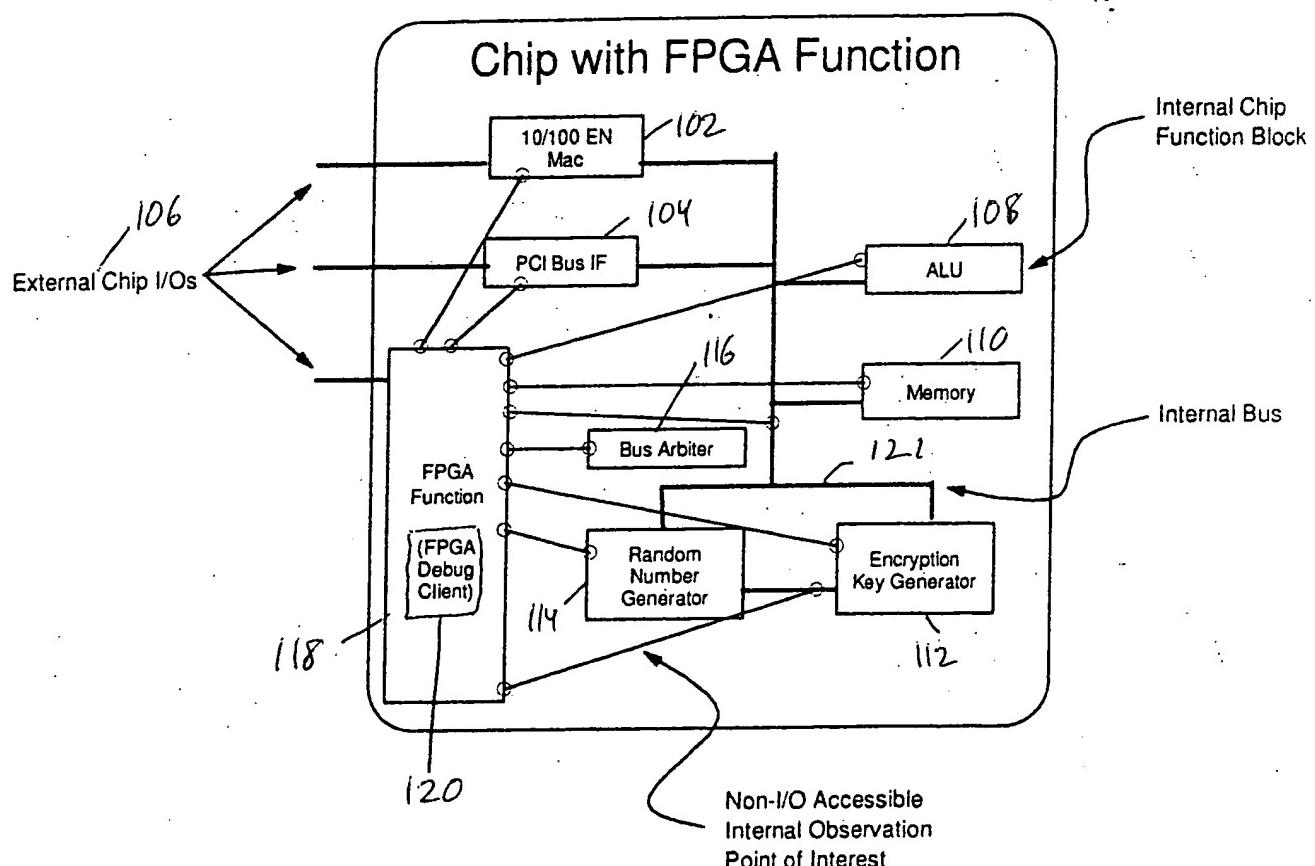
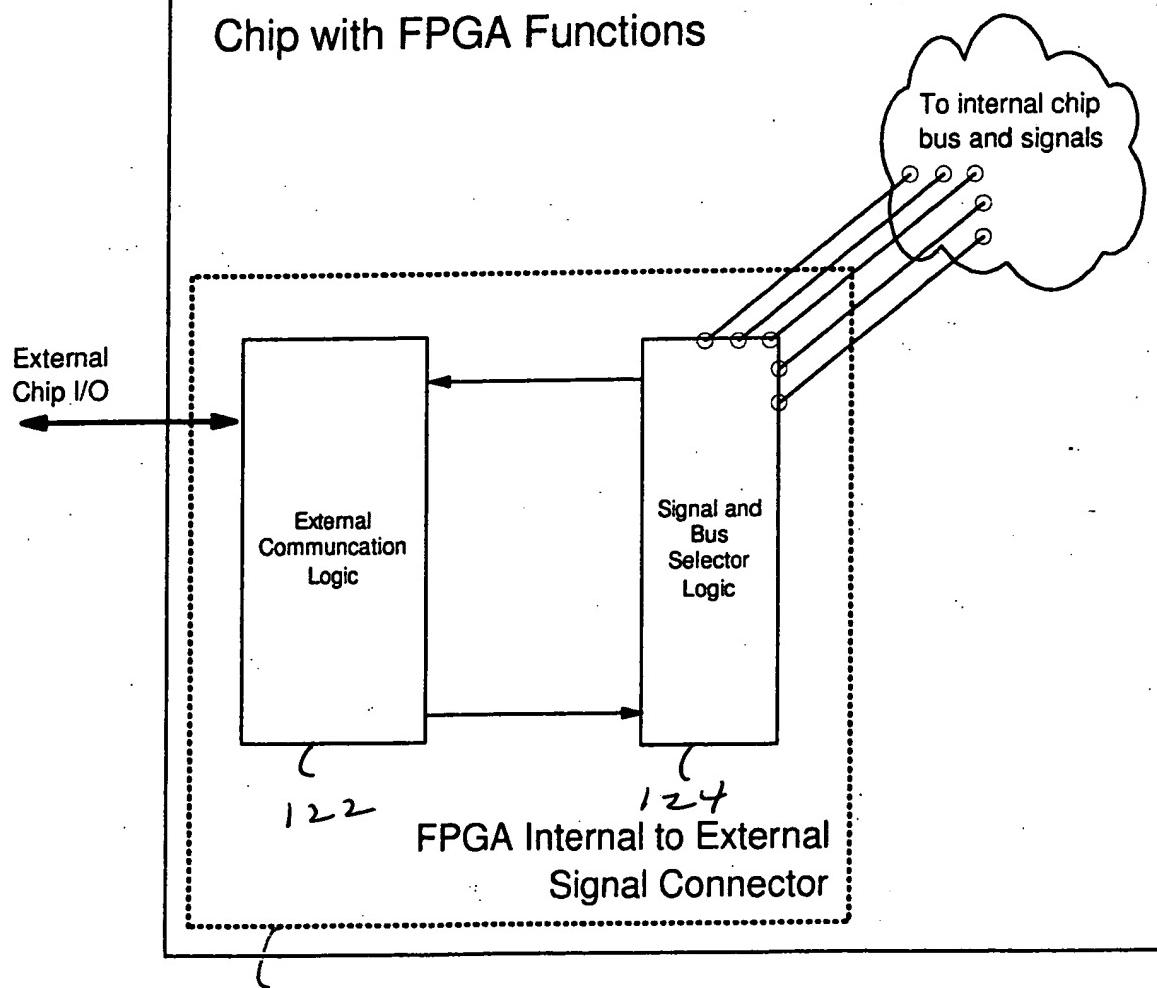
100

Fig. 1

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Chip with FPGA Functions



120

Fig. 2

Monitoring internal bus reveals Function A is not sending correct values to Function B.

Drive correct values into Function B from FPGA and monitor output of Function B to determine if it is operating correctly

External Chip I/Os

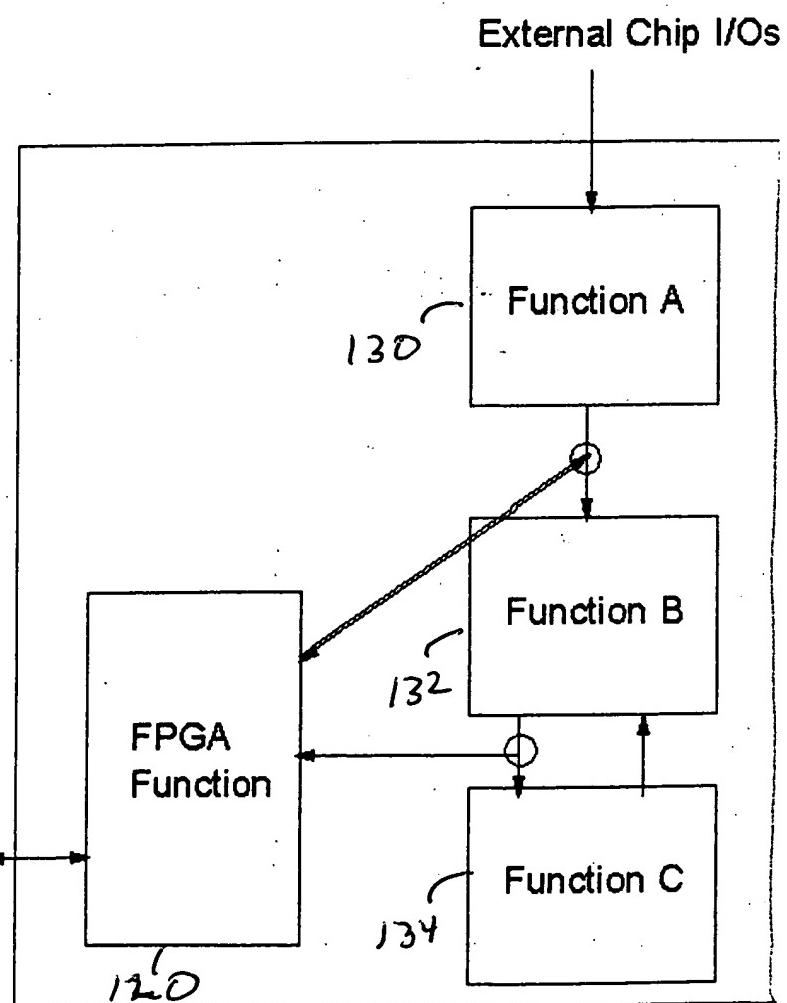


Fig. 3

- Memory and Memory Bus Debug
- MAC Bus Debug
- PCI, Internal Bus and Bus Arbiter Debug
- Processor Debug
- Clock and Clock Distribution Debug

1001164468-12100

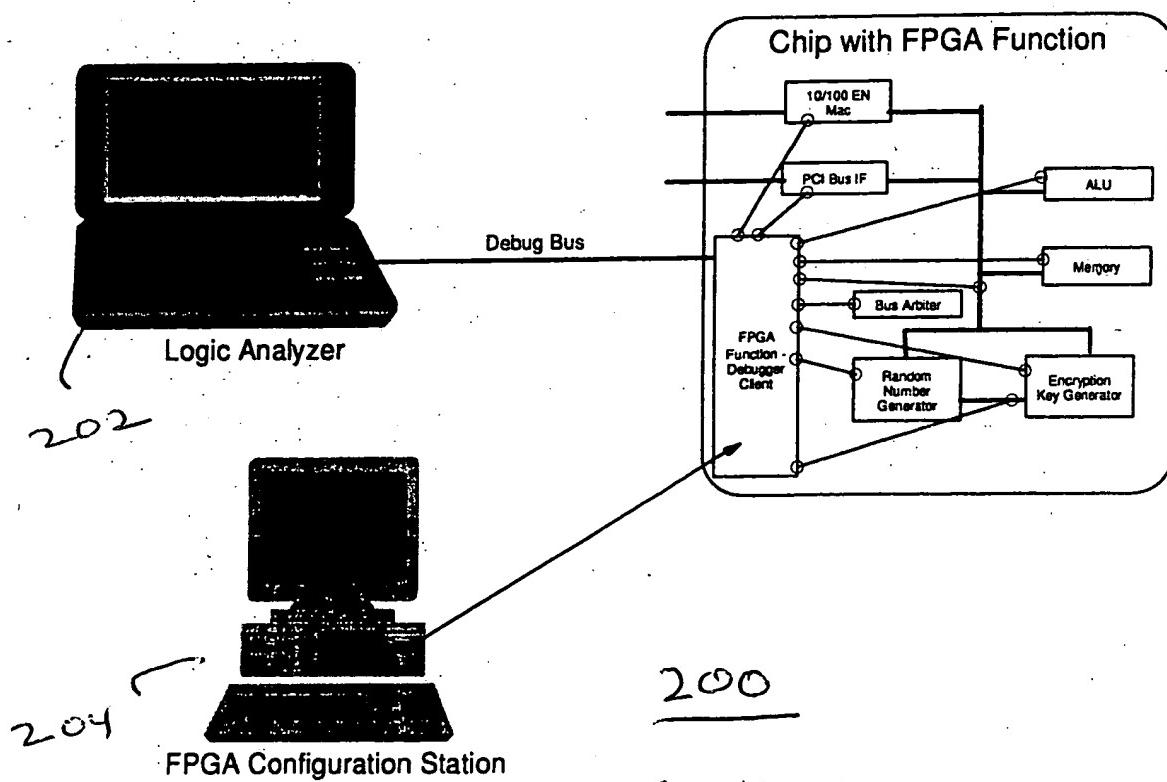
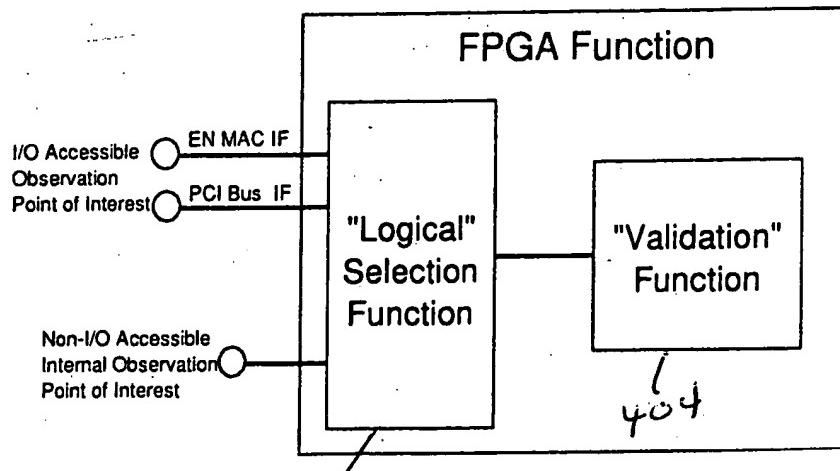


Fig. 4

TODD TEE "344-3476094"



402 Fig. 5

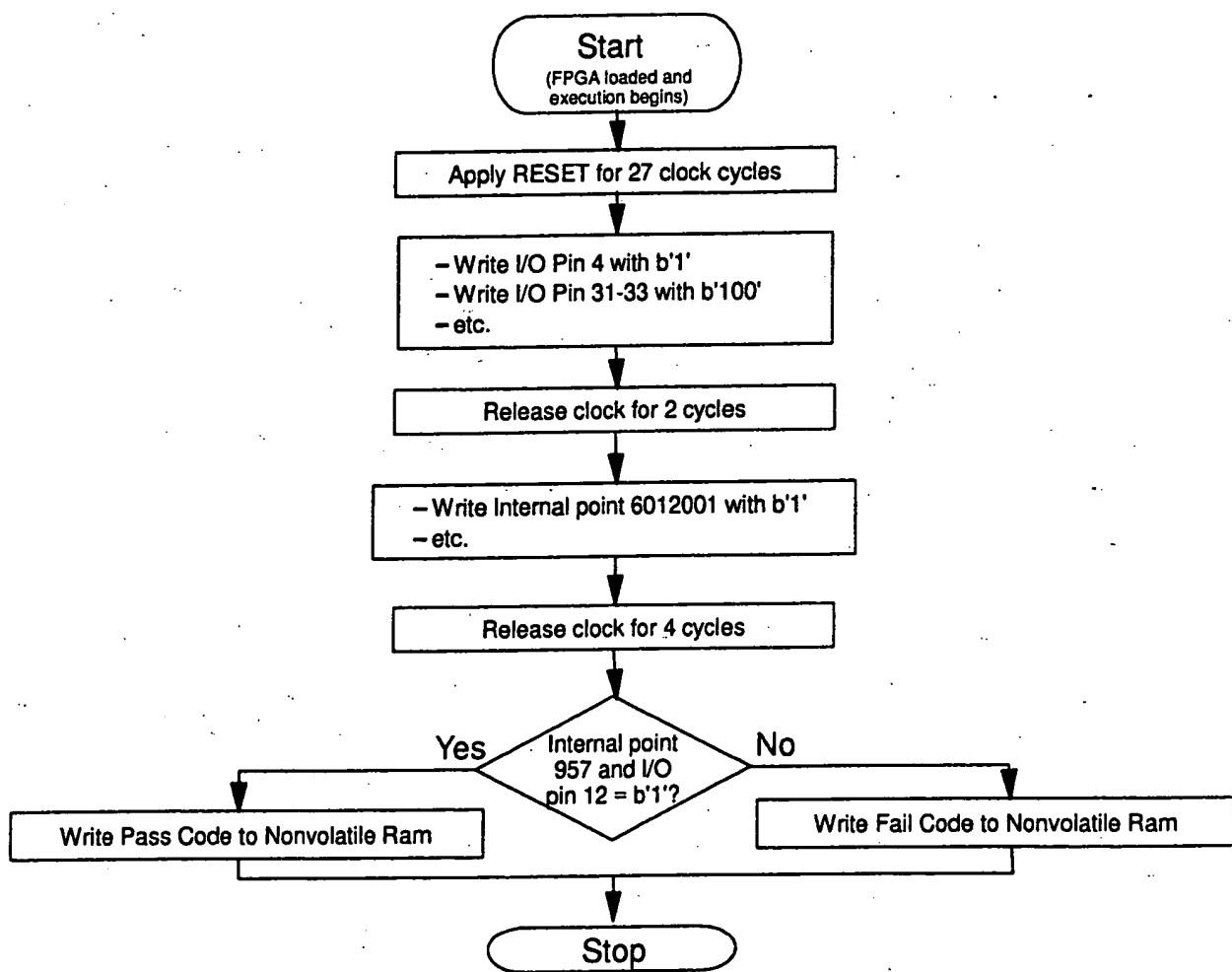


Fig. 6

10016438 - 420001

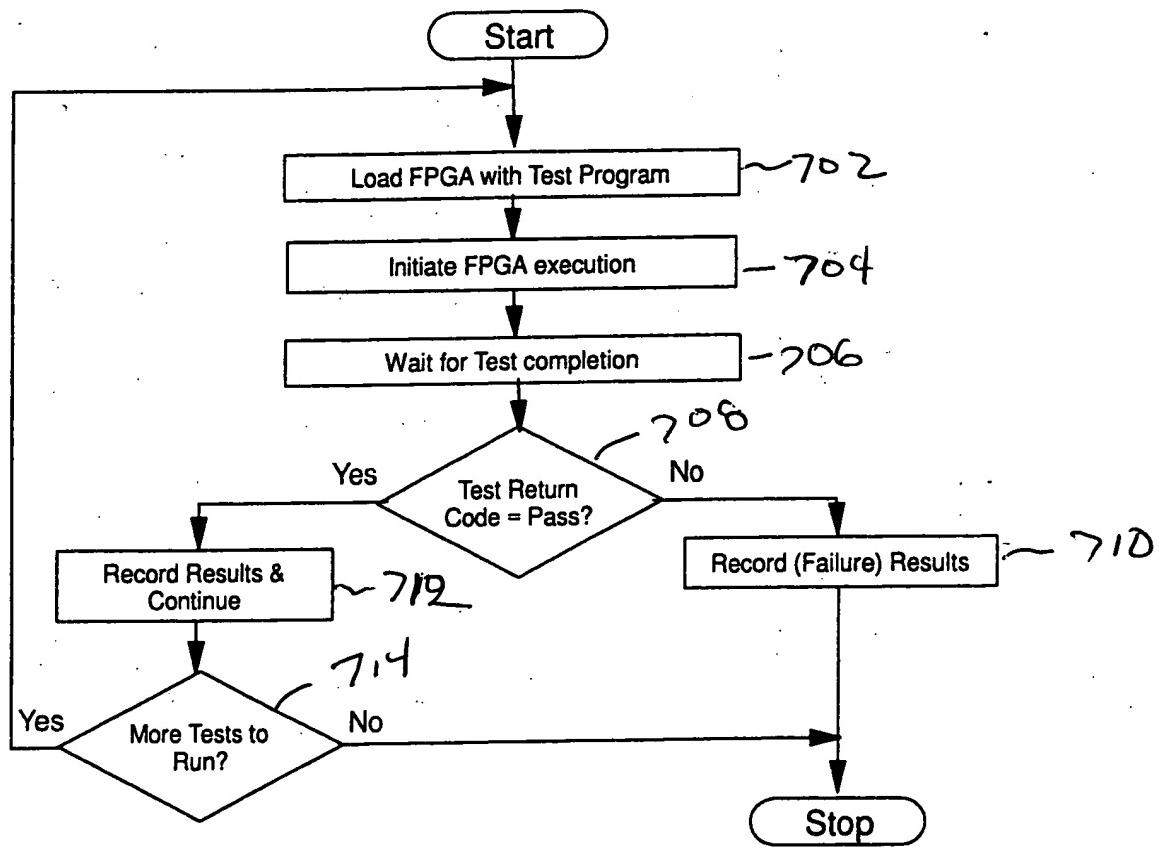


Fig. 7

正月三十日

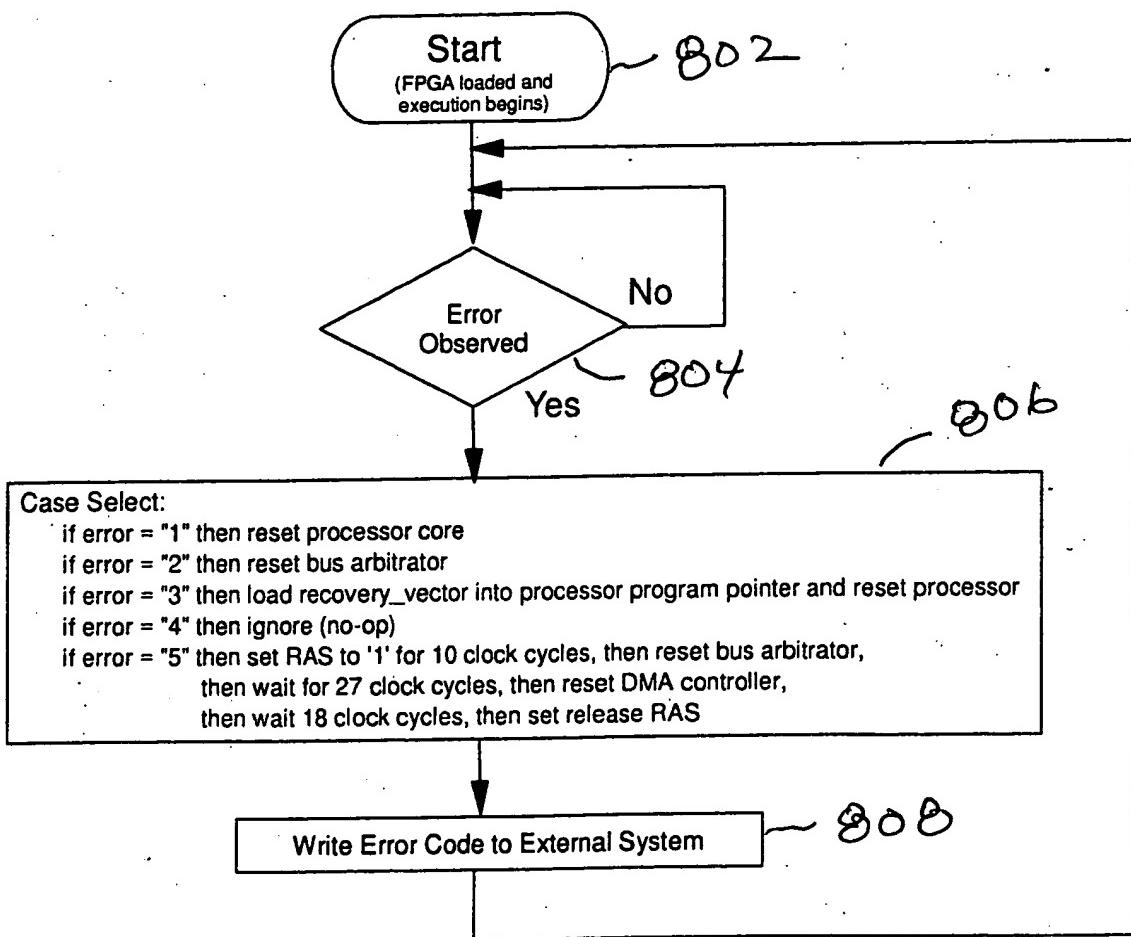


Fig. 8

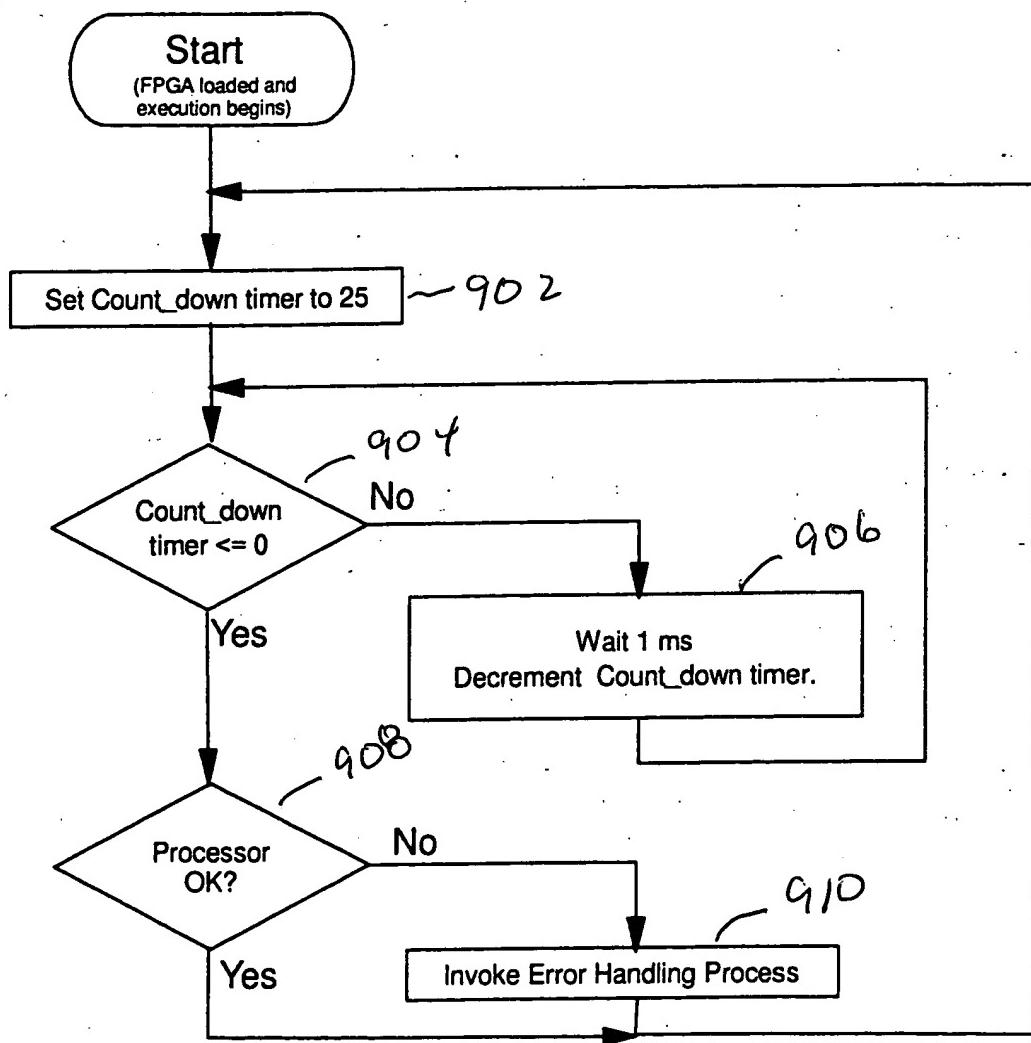


Fig. 9